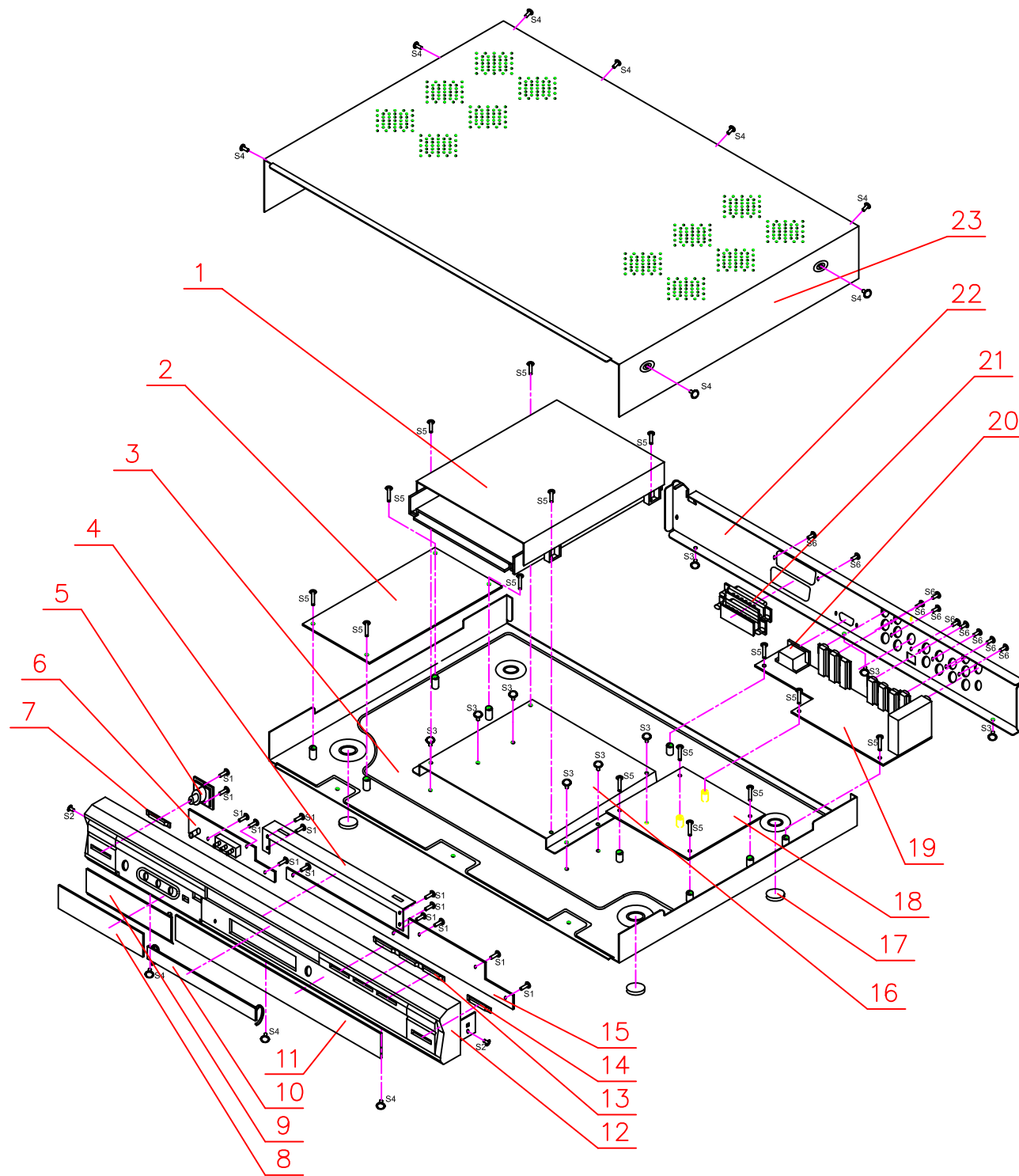
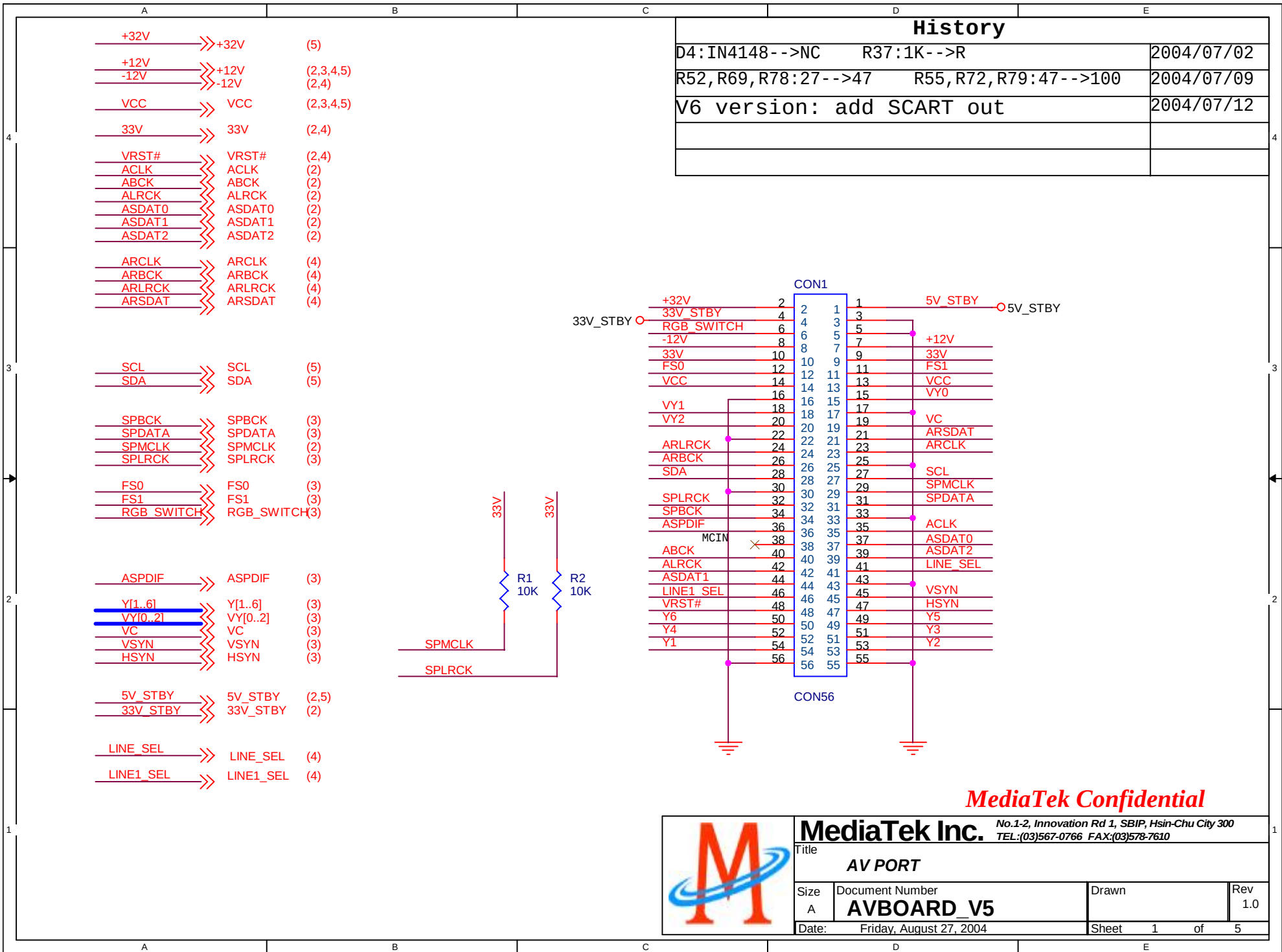




俄罗斯Elenberg
LOGO: ELENBERG
MODEL: DVDR-610

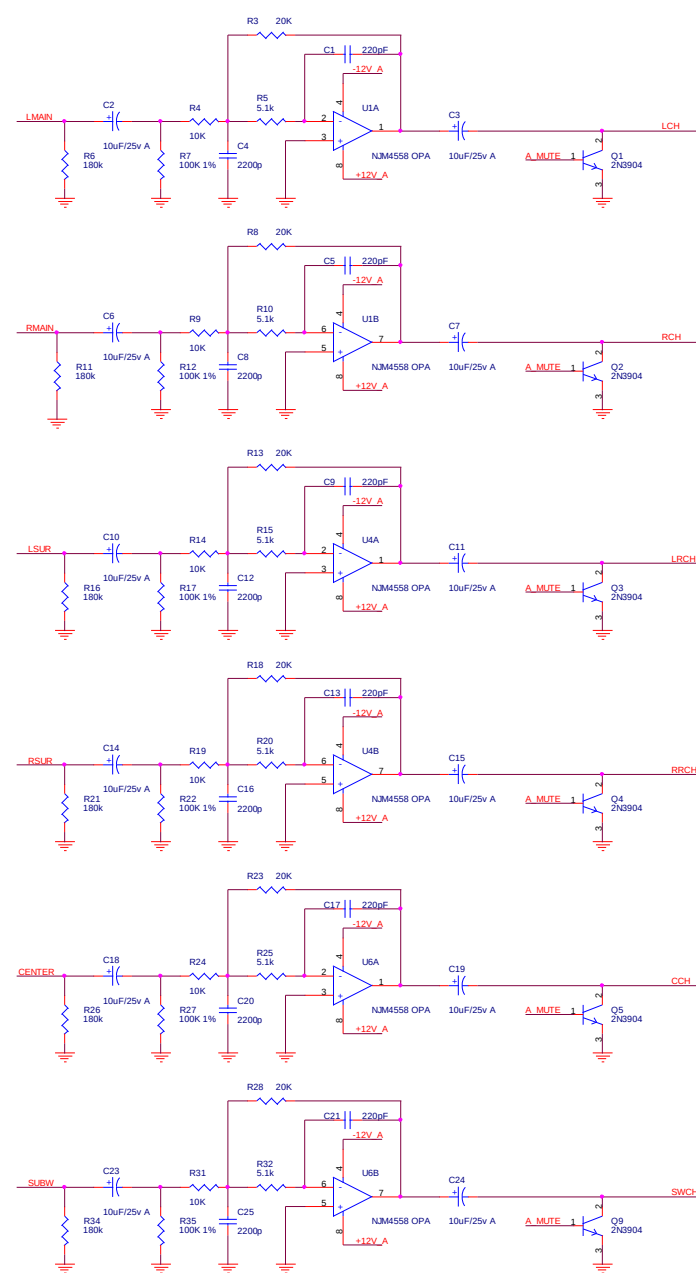
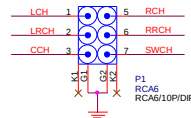
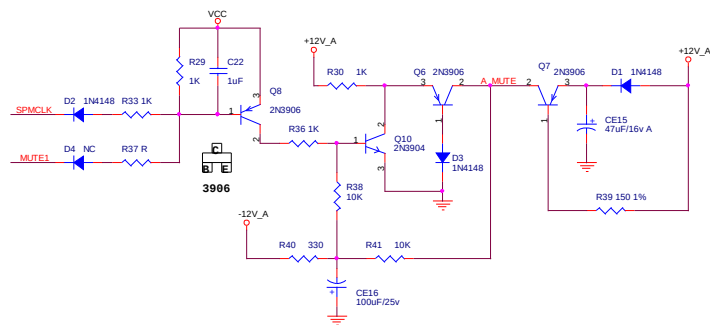
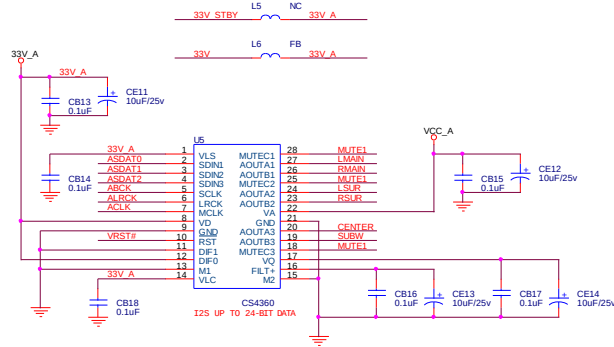
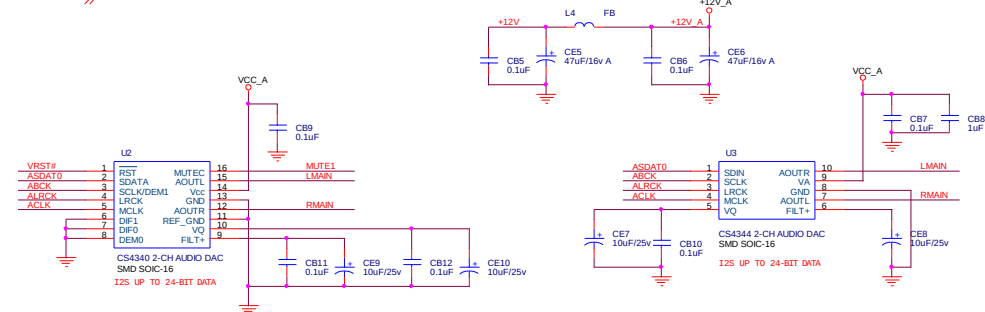
Item	Description	Qty	Item	Description	Qty
23	COVER	1			
22	BACKBOARD	1			
21	SCART	1			
20	VGA	1	S1	3×8PTHNi+	14
19	Export board	1	S2	3×6FMHNI+	2
18	INPUT ASS'Y	1	S3	3×4PWMHNI+	9
17	WASHER	4	S4	3×6PWMHNI+	11
16	BRACKET	1	S5	3×6PMHNI+	11
15	DISPLAY PCB	1	S6	3×8PTHNi+	9
14	RIGHT KEY	1			
13	FUNCTION KEY	1			
12	PANEL	1			
11	LENS(2)	1			
10	DISK DOOR	1			
9	DECORATION DOOR	1			
8	lens(1)	1			
7	LEFT KEY	1			
6	Export board	1			
5	POWER SWITCH	1			
4	bracket of front panel	1			
3	MOTHERBOARD	1			
2	power board	1			
1	Mechanism Ass'y	1			

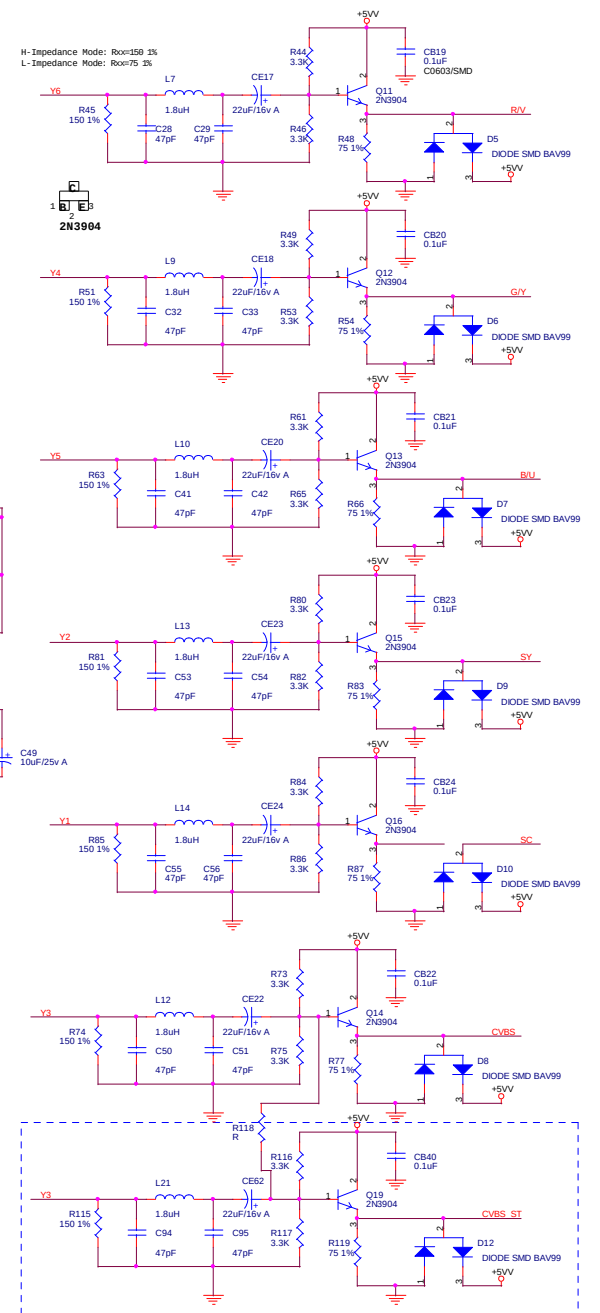
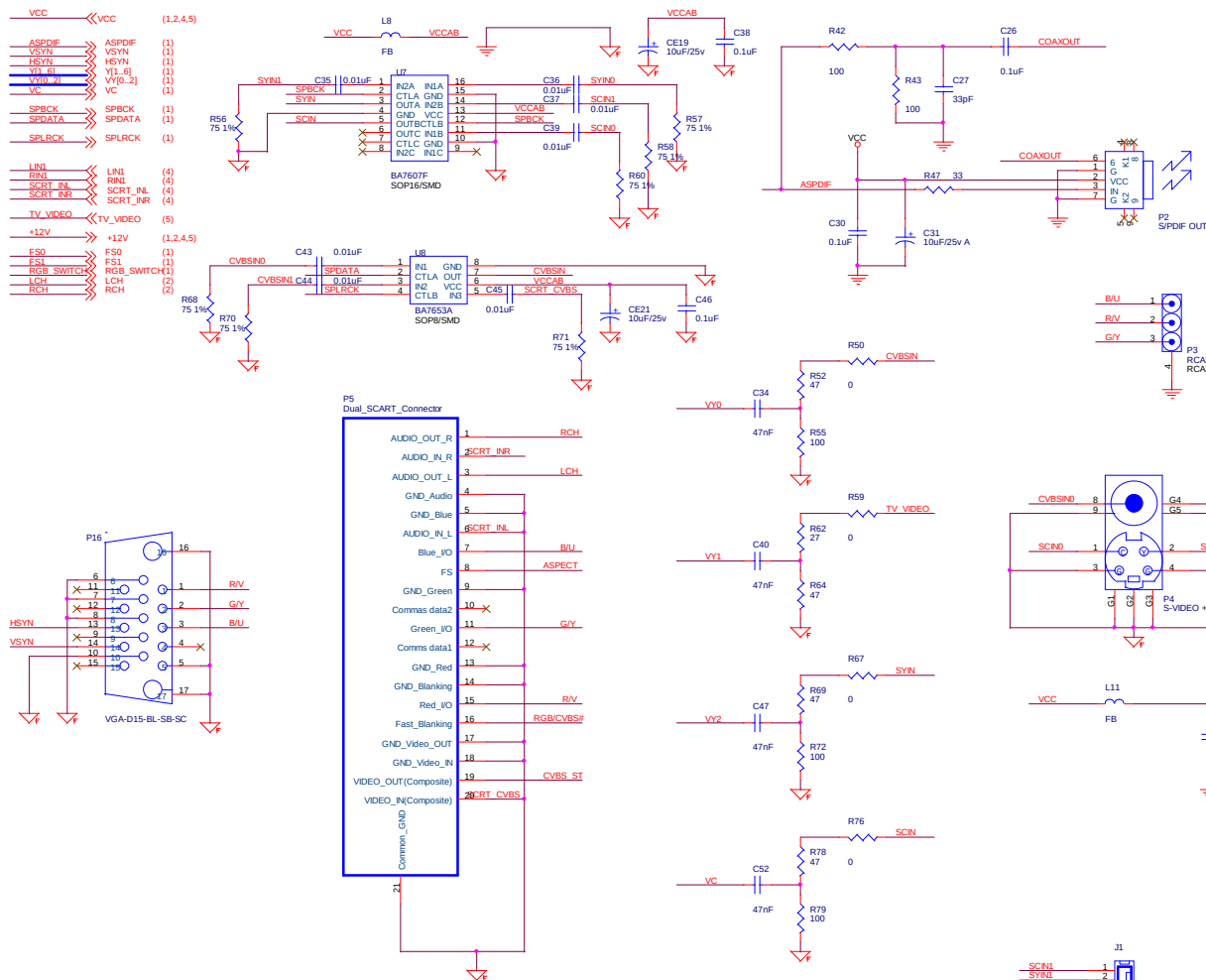


VCC 3.3V (1,3,4,5)
 5V_STBY 5V_STBY (1,5)
 33V_STBY 33V_STBY (1)
 +12V +12V (1,3,4,5)
 -12V -12V (1,4)
 33V 33V (1,4)

VRST# VRST# (1,4)
 ACLK ACLK (1)
 ALCK ALCK (1)
 ASDAT0 ASDAT0 (1)
 ASDAT1 ASDAT1 (1)
 ASDAT2 ASDAT2 (1)
 SPMCLK SPMCLK (1)

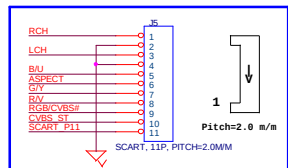
LCH LCH (3)
 RCH RCH (3)



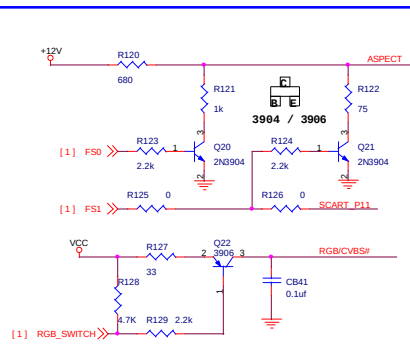


For SCART OUT Connector

SCART CONNECTOR



SCART CONTROL



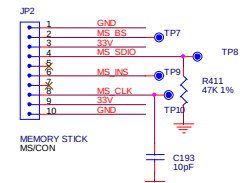
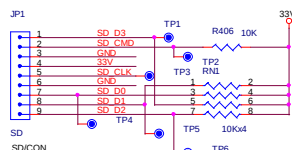
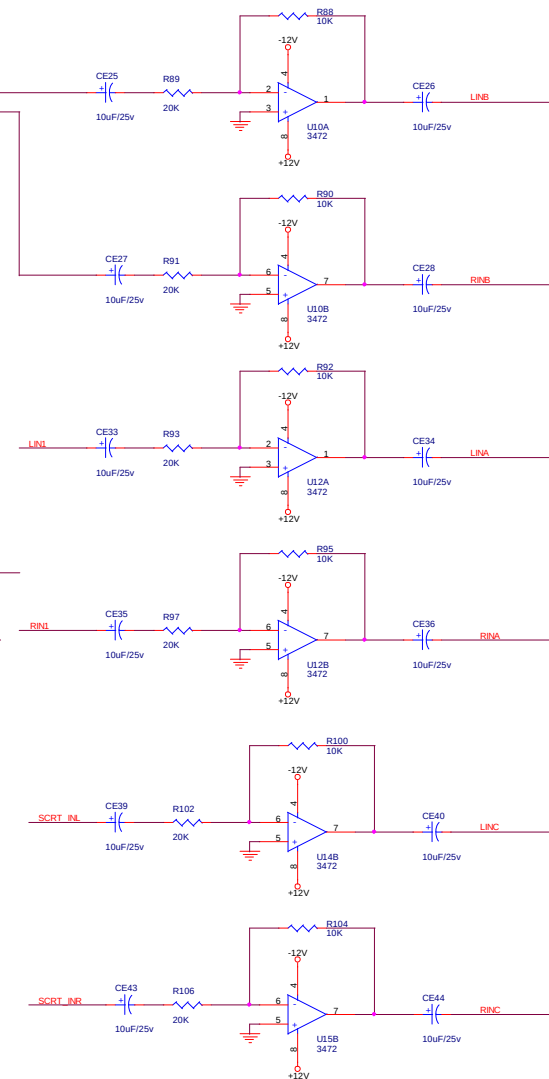
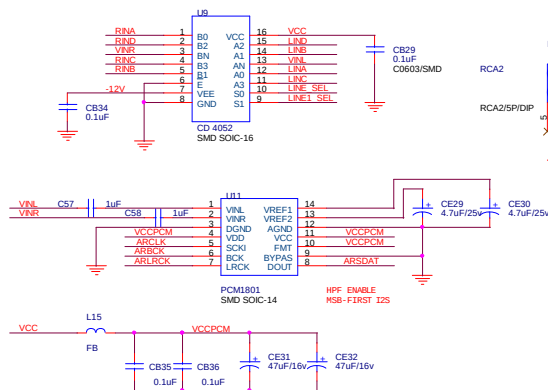
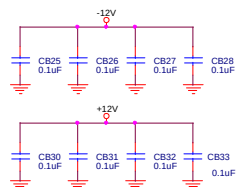
MediaTek Inc.		IF, No.12, Innovation Rd 1, SGP, Hsin-Chu City 300	
TEL: 03567-0766 FAX: 03567-0767			
Title		IN&OUTPUT_VIDEO FILTE&SPDIF	
Size	C	Document Number	AVBOARD V5
Date	Friday, August 27, 2004	Rev	2.2
Sheet	3	of	5

+12V >>> +12V (1,2,5)
 -12V >>> -12V (1,2)
 VCC >>> VCC (1,2,3,5)
 33V >>> 33V (1,2)

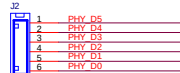
LINA >>> LINA (3)
 RINA >>> RINA (3)
 SCRT_IN >>> SCRT_IN (3)
 TUNER_IN >>> TUNER_IN (3)
 TUNER_IN1 >>> TUNER_IN1 (5)

ARCLK >>> ARCLK (1)
 ARBCK >>> ARBCK (1)
 ARLCK >>> ARLCK (1)
 ARSDAT >>> ARSDAT (1)
 VRS17 >>> VRS17 (1,2)

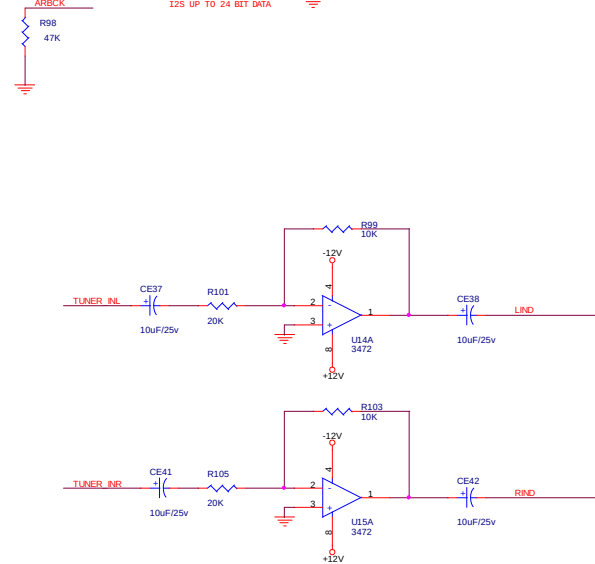
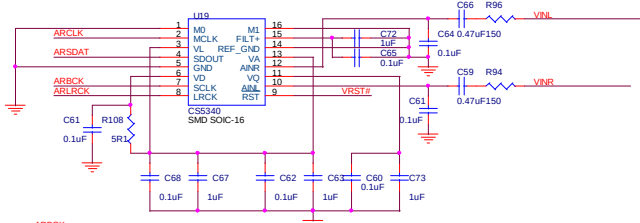
LINE_SEL >>> LINE_SEL (1)
 LINE1_SEL >>> LINE1_SEL (1)

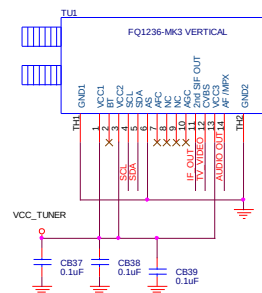


MS_BS R417 33 PHY D1
 MS_SDO R418 33 PHY D2
 MS_CLK R419 33 PHY D0
 SD_CMD R420 33 PHY D3
 SD_CLK R421 33 PHY D5
 SD_D0 R422 33 PHY D4



For SD/MS Card connector, no add now!



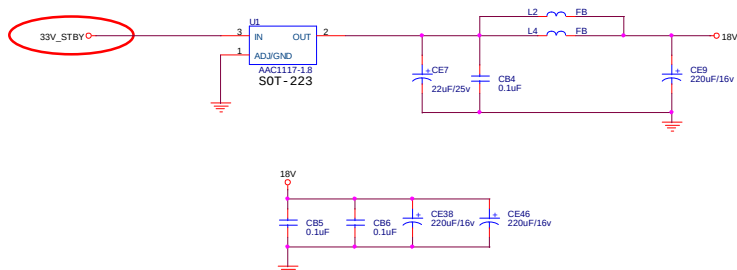
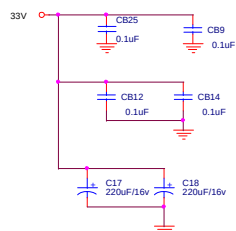
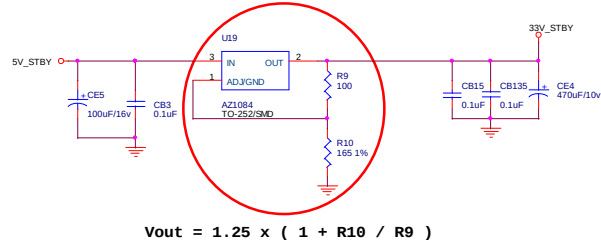
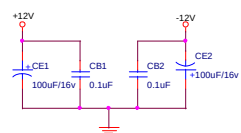
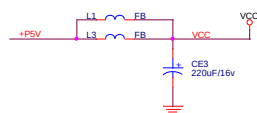
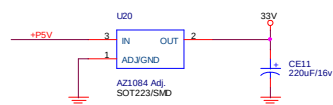
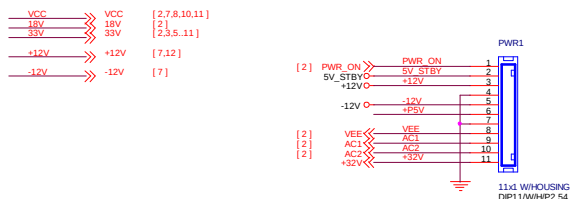


	MediaTek Inc.		1F, No.13, Innovation Rd 1, SBP, Hsin-Chu City 300 TEL: (03)567-0766 FAX: 56909760	
	File			
	TUNER & SOUND MODULE			
	Size C	Document Number AVBOARD V5		Rev 2.2
Date: Friday, August 27, 2004		Sheet 5 of 5		

MT8105B (LQFP256) DVR DEMO BOARD 2 LAYERS

- 1 INDEX & POWER, MUTE
- 2 MT8105B
- 3 MEMORY
- 4 FLASH
- 5 IDE
- 6 AUDIO DAC
- 7 1394&USB
- 8 OUTPUT

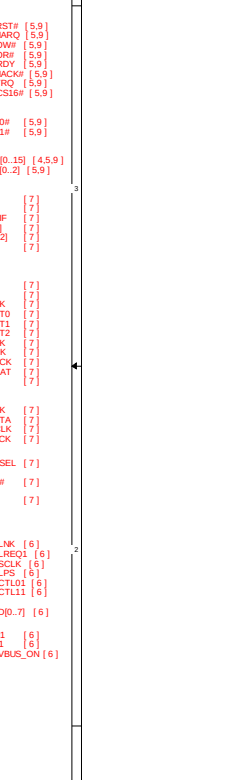
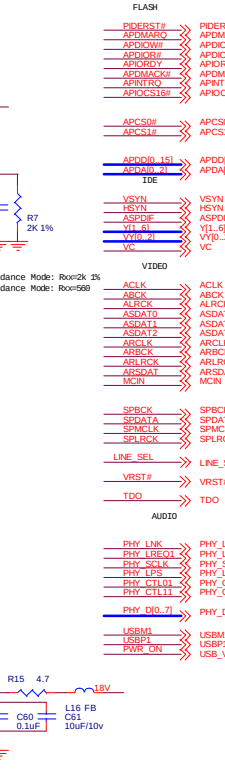
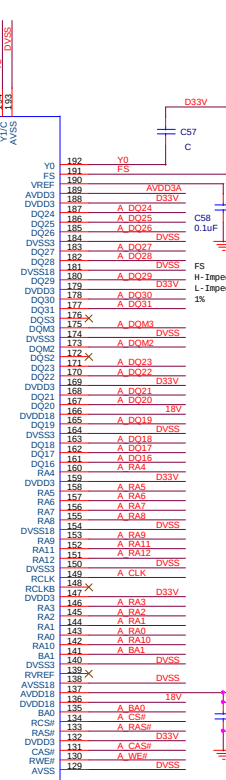
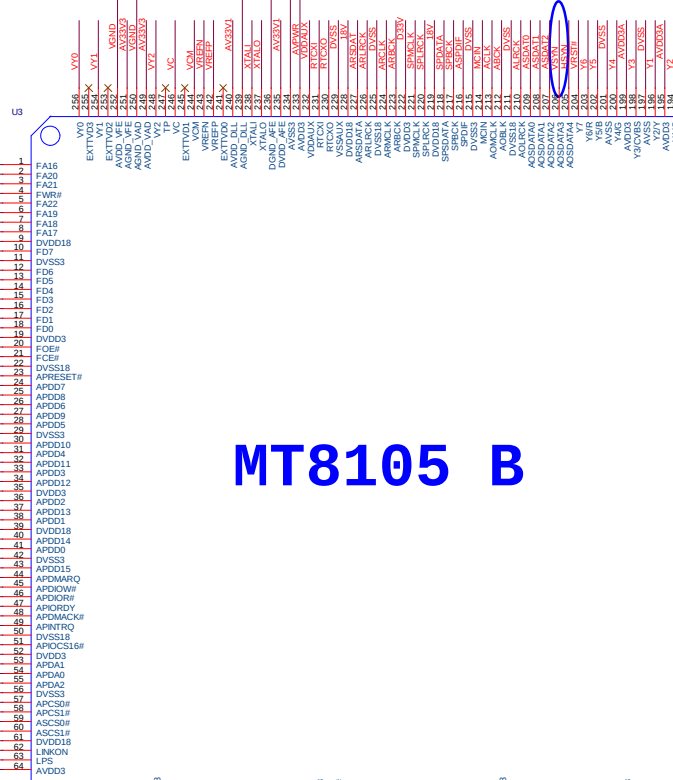
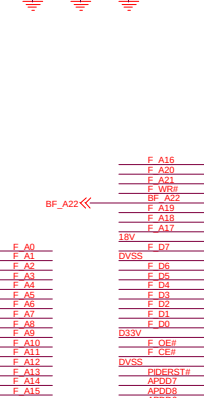
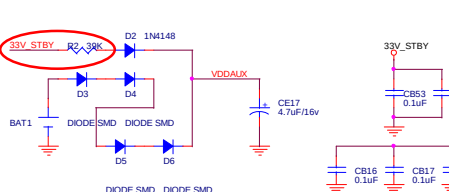
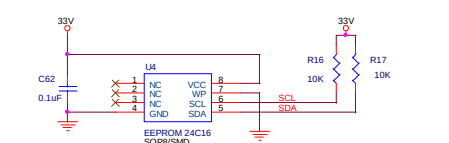
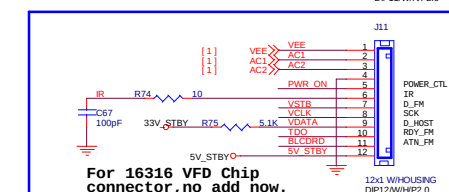
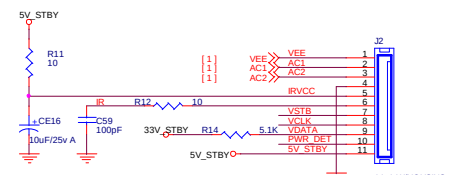
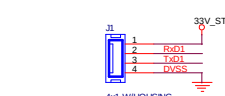
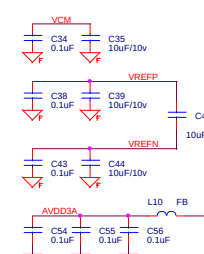
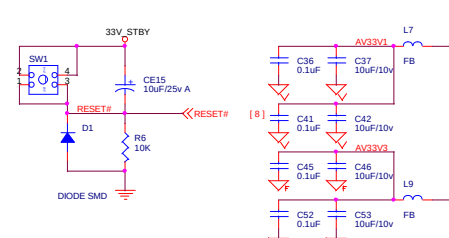
Rev	History	P#	Date
V1	The original released.		2004/03/18
V1.1	Change U17's input source from 33V to VCC. Change R94, R96 and R97 to 15K Ohm. Add R2, R3 and R145 Pull-down resistors on net IIN3, IIN2 and IINR. Swap CON3's odd pin and even pin. Change CON3 from 48 pin to 50 pin. Add +12V and -12V net in CON3's pin-1 and pin-2.	8 11 11 7 7 7	2004/04/01
V2			2004/04/27
V5	Change U19 from AAC1117 to AZ1084. Change U19's package from SOT223 to TO-252. Change U1 input power from 5V_STBY to 33V_STBY. Change D2's pin-1 connection from 18V to DV25. Change MT1818, MT8105 crystal circuit for thrice resonance crystal. Change J5 connection for new K-POWER.	1 1 1 2	2004/05/17
V6	Add SCART out control pin. Add USB Vbus chip. Change RTC Power net Add VGA output.		2004/07/12 2004/07/22 2004/07/27



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Size	Document Number	Drawn	Rev
C	MT8105SDR_SANYO_V5		2.1
Date	Updated: 3/27/2004	Sheet	1 of 12

PWR_ON <<PWR_ON [1]
 VCC <<VCC [1,7,8,10,11]
 33V <<33V [1]
 33V <<33V [1,3,5,11]



MT8105 B

For USB Down Load

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33V 33V [1,2,5,11]

A_BA0_12] A_RA0_12] [2]
A_BA0_11] A_BA0_11] [2]
A_DQM0_3] A_DQM0_3] [2]
A_DQM0_11] A_DQM0_11] [2]
A_CLK A_CLK [2]
A_CKE A_CKE [2]
A_CS# A_CS# [2]
A_RAS# A_RAS# [2]
A_CAS# A_CAS# [2]
A_WE# A_WE# [2]

RN1
A_RA3 1 2 SA_RA3
A_RA4 3 4 SA_RA4
A_RA5 5 6 SA_RA5
A_RA6 7 8 SA_RA6

RN2
A_RA4 1 2 SA_RA4
A_RA5 3 4 SA_RA5
A_RA6 5 6 SA_RA6
A_RA7 7 8 SA_RA7

RN3
A_RA8 1 2 SA_RA8
A_RA9 3 4 SA_RA9
A_RA10 5 6 SA_RA10
A_RA11 7 8 SA_RA11
A_RA12 9 10 SA_RA12

R20
A_RA10 1 2 SA_RA10

RN4
A_DO3 1 2 SA_DO3
A_DO2 3 4 SA_DO2
A_DO1 5 6 SA_DO1
A_DO0 7 8 SA_DO0

RN5
A_DO7 1 2 SA_DO7
A_DO6 3 4 SA_DO6
A_DO5 5 6 SA_DO5
A_DO4 7 8 SA_DO4

RN6
A_DO8 1 2 SA_DO8
A_DO9 3 4 SA_DO9
A_DO10 5 6 SA_DO10
A_DO11 7 8 SA_DO11

RN7
A_DO12 1 2 SA_DO12
A_DO13 3 4 SA_DO13
A_DO14 5 6 SA_DO14
A_DO15 7 8 SA_DO15

RN8
A_DO19 1 2 SA_DO19
A_DO18 3 4 SA_DO18
A_DO17 5 6 SA_DO17
A_DO16 7 8 SA_DO16

RN9
A_DO23 1 2 SA_DO23
A_DO22 3 4 SA_DO22
A_DO21 5 6 SA_DO21
A_DO20 7 8 SA_DO20

RN10
A_DO24 1 2 SA_DO24
A_DO25 3 4 SA_DO25
A_DO26 5 6 SA_DO26
A_DO27 7 8 SA_DO27

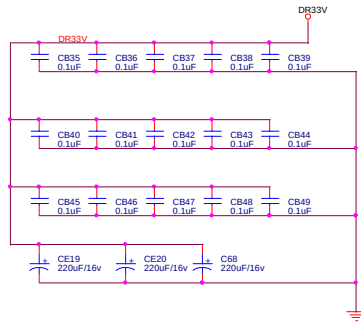
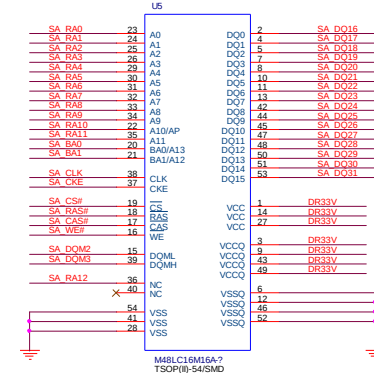
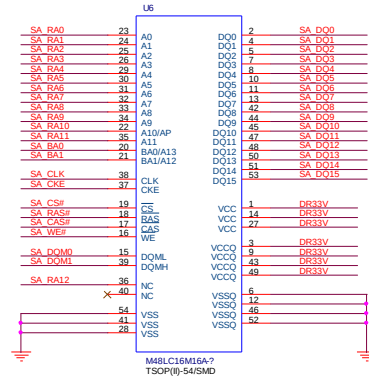
RN11
A_DO28 1 2 SA_DO28
A_DO29 3 4 SA_DO29
A_DO30 5 6 SA_DO30
A_DO31 7 8 SA_DO31

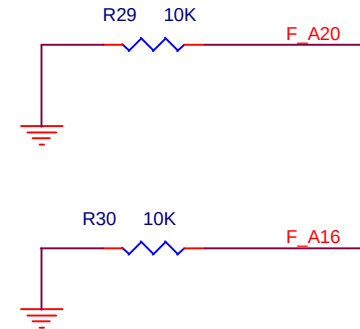
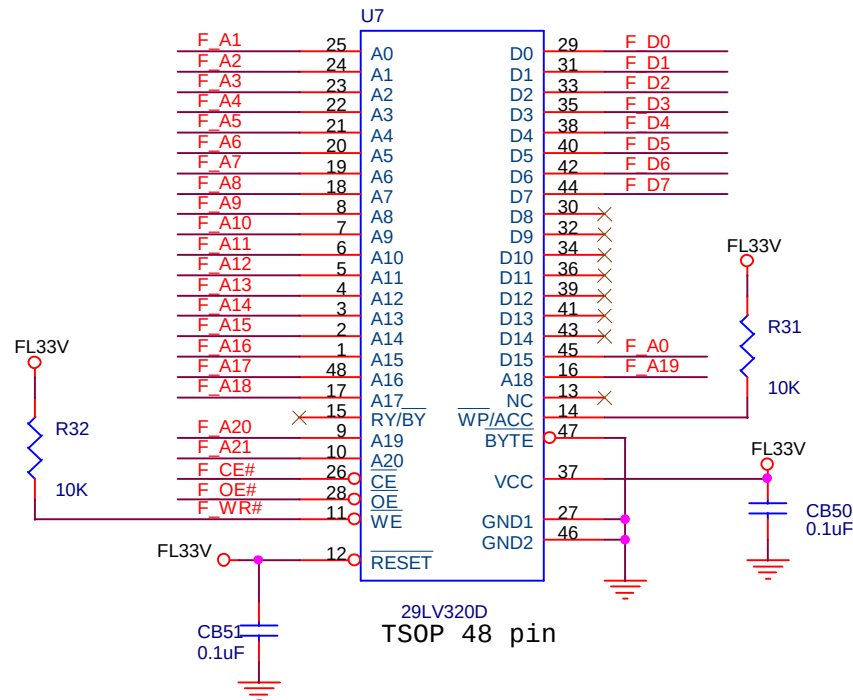
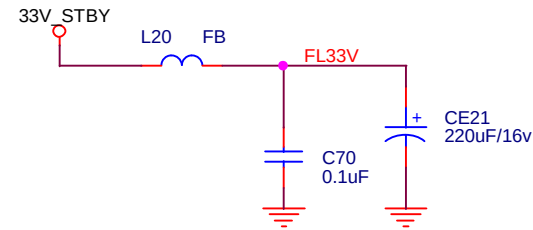
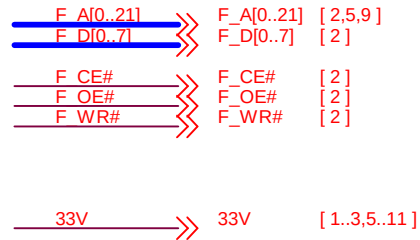
RN12
A_CS# 1 2 SA_CS#
A_RAS# 3 4 SA_RAS#
A_CAS# 5 6 SA_CAS#
A_WE# 7 8 SA_WE#

A_BA1R21 33 SA_BA1
A_BA0R22 33 SA_BA0

A_CKE R23 10 SA_CKE
A_CLK R24 10 SA_CLK

A_DQM0 R25 33 SA_DQM0
A_DQM1 R26 33 SA_DQM1
A_DQM2 R27 33 SA_DQM2
A_DQM3 R28 33 SA_DQM3





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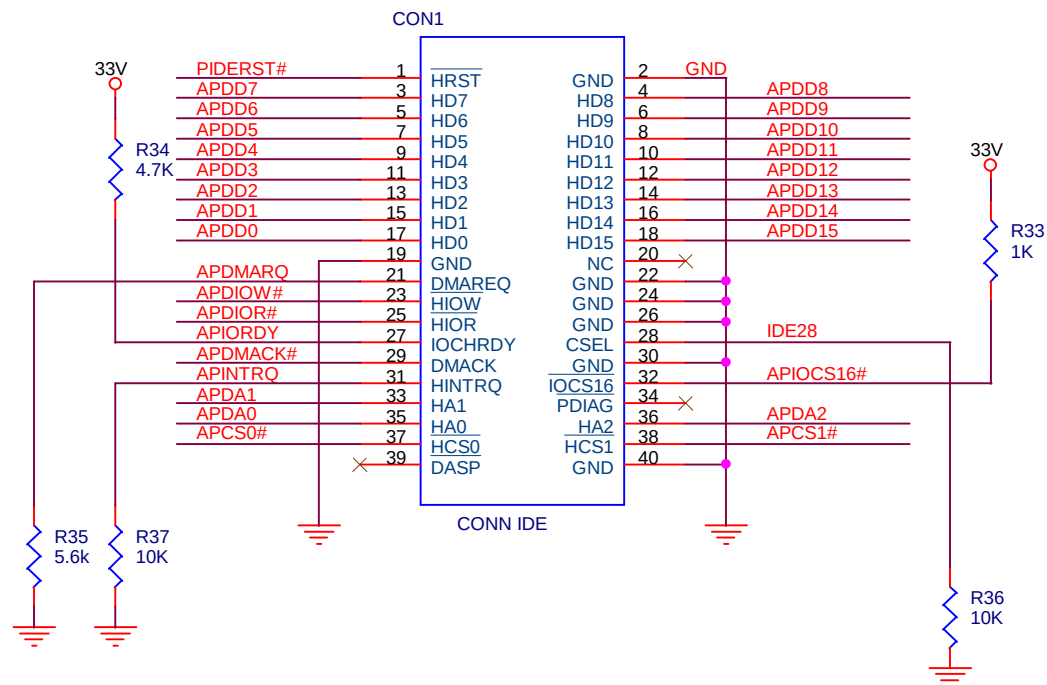


MediaTek Inc.		No.1-2, Innovation Rd 1, SBIP, Hsin-Chu City 300 TEL: (03)567-0766 FAX: (03)578-7610	
Title FLASH			
Size A	Document Number MT8105SDR_SANYO_V5	Drawn	Rev 2.1
Date: Tuesday, July 27, 2004	Sheet 4 of 12		

33V >> 33V [1..3,6..11]

PIDERST# >> PIDERST# [2,9]
APDMARQ >> APDMARQ [2,9]
APDIOW# >> APDIOW# [2,9]
APDIOR# >> APDIOR# [2,9]
APIORDY >> APIORDY [2,9]
APDMACK# >> APDMACK# [2,9]
APINTRQ >> APINTRQ [2,9]
APIOCS16# >> APIOCS16# [2,9]

APCS0# >> APCS0# [2,9]
APCS1# >> APCS1# [2,9]
APDD[0..15] >> APDD[0..15] [2,4,9]
APDA[0..2] >> APDA[0..2] [2,9]



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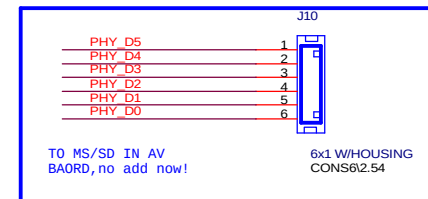
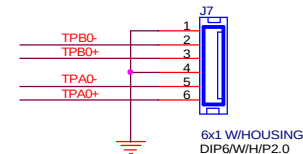
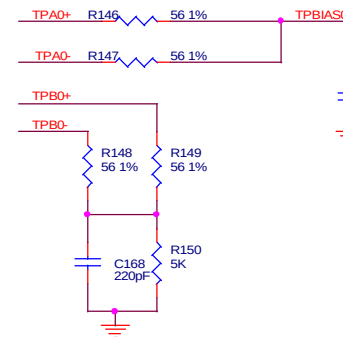
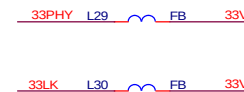
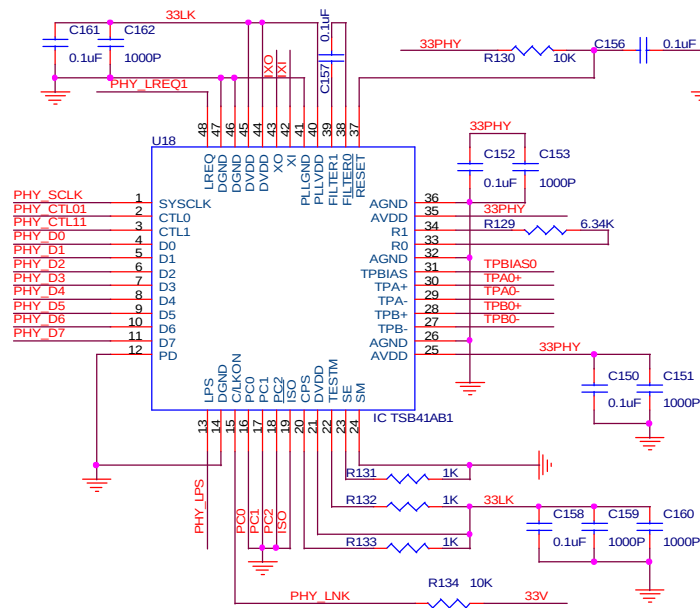
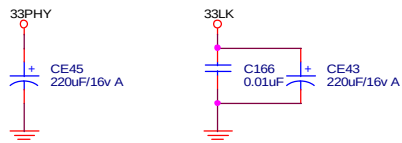
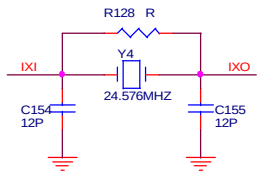
MediaTek Inc.		No.1-2, Innovation Rd 1, SBIP, Hsin-Chu City 300 TEL: (03)567-0766 FAX: (03)578-7610	
Title IDE I/F			
Size A	Document Number MT8105BSD_V5mainboard	Drawn	Rev 2.1
Date:	Tuesday, July 27, 2004	Sheet 5 of 12	

VCC → VCC [1,2,7,8,10,11]
33V → 33V [1..3,5,7..11]

PHY_LNK → PHY_LNK [2]
PHY_LREQ1 → PHY_LREQ1 [2]
PHY_SCLK → PHY_SCLK [2]
PHY_LPS → PHY_LPS [2]
PHY_CTL01 → PHY_CTL01 [2]
PHY_CTL11 → PHY_CTL11 [2]
PHY_D10..71 → PHY_D[0..7] [2]

USB_VBUS_ON → USB_VBUS_ON [2]
USBM1 → USBM1 [2]
USBP1 → USBP1 [2]

33V → C71 0.1uF



TO MS/SD IN AV
BAORD, no add now!

6x1 W/HOUSING
DIP6W/H/P2.0

For USB function, no add now!

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MediaTek Inc. No.1-2, Innovation Rd 1, SBIP, Hsin-Chu City 300 TEL: (03) 567-0766 FAX: (03) 567-7610	
Title 1394 & USB I/F	
Size B	Document Number MT8105SDR_SANYO_V5
Date: Tuesday, July 27, 2004	Sheet 6 of 12
Drawn	Rev 2.1

+32V >>> +32V
+12V >>> +12V [1,12]
-12V >>> -12V [1]
VCC >>> VCC [1,2,8,10,11]
33V >>> 33V [1..3,5,6,8..11]

ACLK >>> ACLK [2]
ABCK >>> ABCK [2]
ALRCK >>> ALRCK [2]
ASDAT0 >>> ASDAT0 [2]
ASDAT1 >>> ASDAT1 [2]
ASDAT2 >>> ASDAT2 [2]

ARCLK >>> ARCLK [2]
ARBCK >>> ARBCK [2]
ARLRCK >>> ARLRCK [2]
ARSDAT >>> ARSDAT [2]
MCIN >>> MCIN [2]

SCL >>> SCL [2]
SDA >>> SDA [2]

SPBCK >>> SPBCK [2]
SPDATA >>> SPDATA [2]
SPMCLK >>> SPMCLK [2]
SPLRCK >>> SPLRCK [2]

LINE_SEL >>> LINE_SEL [2]

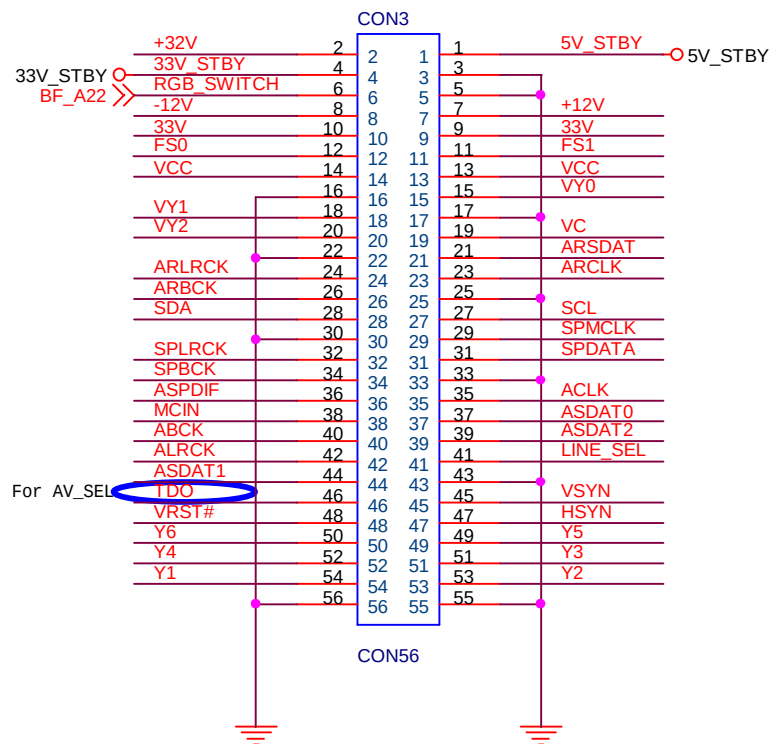
VRST# >>> VRST# [2]

TDO >>> TDO [2]

ASPDIF >>> ASPDIF [2]

Y[1..6] >>> Y[1..6] [2]
VY[0..2] >>> VY[0..2] [2]
VC >>> VC [2]
VSYN >>> VSYN [2]
HSYN >>> HSYN [2]

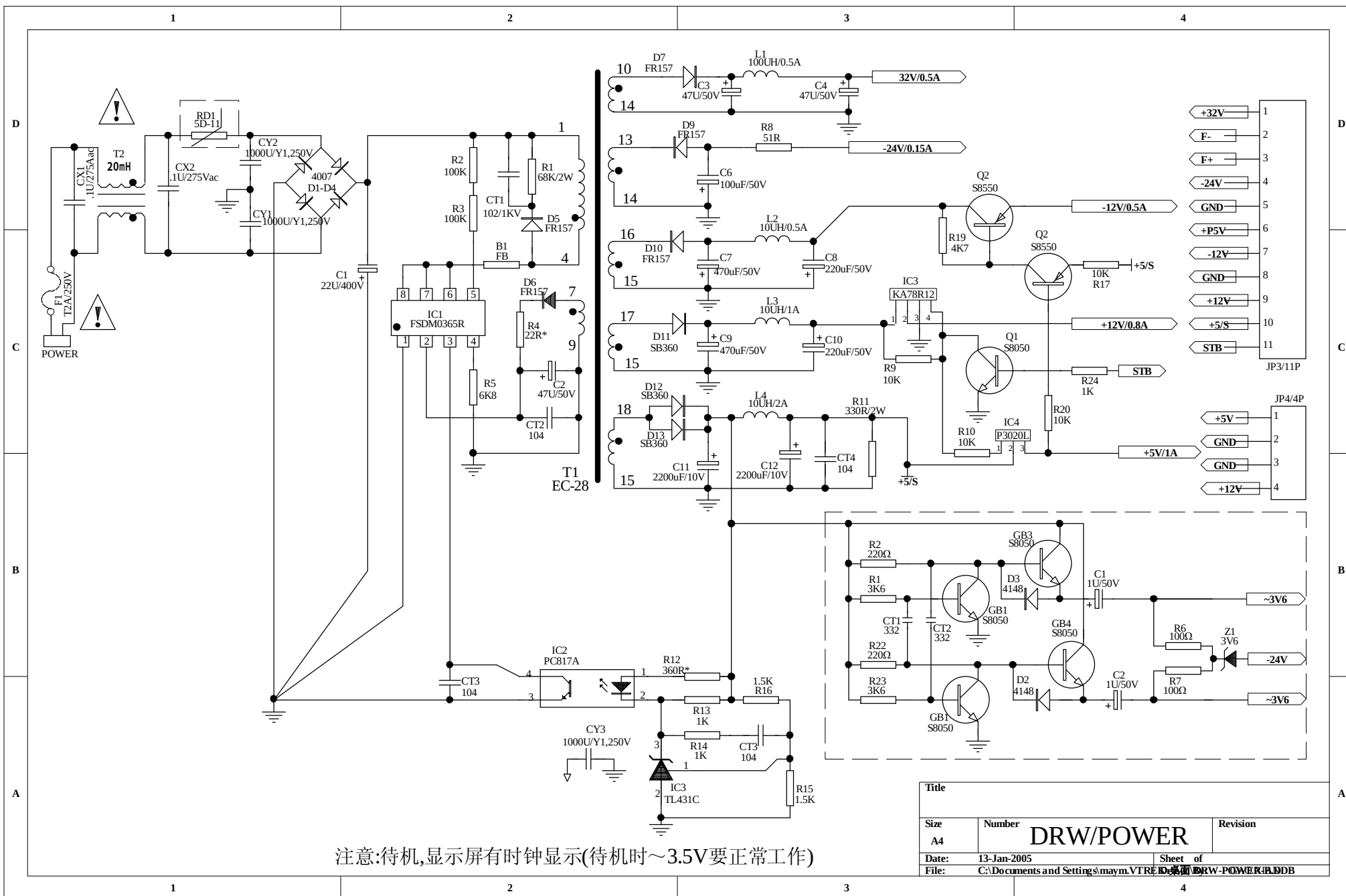
FS0 >>> FS0 [2]
FS1 >>> FS1 [2]



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